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NOTICE OF ALLOWANCE AND FEE(S) DUE

2292

7590

04/11/2008

BIRCH STEWART KOLASCH & BIRCH PO BOX 747 FALLS CHURCH, VA 22040-0747 EXAMINER

RUDE, TIMOTHY L

ART UNIT PAPER NUMBER

2871 DATE MAILED: 04/11/2008

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/677,870	10/03/2000	Woo Hyuk Choi	2658-0240P	6124

TITLE OF INVENTION: LIQUID CRYSTAL DISPLAY WITH PARTICULAR GATE DUMMY PATTERNS TO FACILITATE REPAIR

APPLN. TYPE	SMALL ENTITY	ISSUE FEE DUE	PUBLICATION FEE DUE	PREV. PAID ISSUE FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1440	\$0	\$0	\$1440	07/11/2008

THE APPLICATION IDENTIFIED ABOVE HAS BEEN EXAMINED AND IS ALLOWED FOR ISSUANCE AS A PATENT. PROSECUTION ON THE MERITS IS CLOSED. THIS NOTICE OF ALLOWANCE IS NOT A GRANT OF PATENT RIGHTS. THIS APPLICATION IS SUBJECT TO WITHDRAWAL FROM ISSUE AT THE INITIATIVE OF THE OFFICE OR UPON PETITION BY THE APPLICANT. SEE 37 CFR 1.313 AND MPEP 1308.

THE ISSUE FEE AND PUBLICATION FEE (IF REQUIRED) MUST BE PAID WITHIN THREE MONTHS FROM THE MAILING DATE OF THIS NOTICE OR THIS APPLICATION SHALL BE REGARDED AS ABANDONED. THIS STATUTORY PERIOD CANNOT BE EXTENDED. SEE 35 U.S.C. 151. THE ISSUE FEE DUE INDICATED ABOVE DOES NOT REFLECT A CREDIT FOR ANY PREVIOUSLY PAID ISSUE FEE IN THIS APPLICATION. IF AN ISSUE FEE HAS PREVIOUSLY BEEN PAID IN THIS APPLICATION (AS SHOWN ABOVE), THE RETURN OF PART B OF THIS FORM WILL BE CONSIDERED A REQUEST TO REAPPLY THE PREVIOUSLY PAID ISSUE FEE TOWARD THE ISSUE FEE NOW DUE.

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III. All communications regarding this application must give the application number. Please direct all communications prior to issuance to Mail Stop ISSUE FEE unless advised to the contrary.

IMPORTANT REMINDER: Utility patents issuing on applications filed on or after Dec. 12, 1980 may require payment of maintenance fees. It is patentee's responsibility to ensure timely payment of maintenance fees when due.

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Complete and send this form, together with applicable fee(s), to: Mail Mail Stop ISSUE FEE

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2292 7590 04/11/2008 BIRCH STEWART KOLASCH & BIRCH PO BOX 747 FALLS CHURCH, VA 22040-0747			I he	Cert	tificate of Mailing o	or Transn	nission deposited with the United class mail in an envelope above, or being facsimile te indicated below.
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							(Signature)
							(Date)
APPLICATION NO.	FILING DATE		FIRST NAMED INVENTOR		ATTORNEY DOCKE	ET NO.	CONFIRMATION NO.
09/677,870 TITLE OF INVENTION:	10/03/2000 : LIQUID CRYSTAL D	ISPLAY WITH PARTI	W∞ Hyuk Choi CULAR GATE DUMMY P	ATTERNS TO FA	2658-0240P CILITATE REPAIR		6124
APPLN. TYPE	SMALL ENTITY	ISSUE FEE DUE	PUBLICATION FEE DUE	PREV. PAID ISSUE	E FEE TOTAL FEI	E(S) DUE	DATE DUE
nonprovisional	NO	\$1440	\$0	\$0	\$14	40	07/11/2008
EXAM	INER	ART UNIT	CLASS-SUBCLASS				
RUDE, TIN	MOTHY L	2871	349-043000	•			
"Fee Address" indi PTO/SB/47; Rev 03-0. Number is required. 3. ASSIGNEE NAME AT	ondence address (or Cha 3/122) attached. ication (or "Fee Address 2 or more recent) attach ND RESIDENCE DATA ess an assignee is ident n in 37 CFR 3.11. Comp	nge of Correspondence "Indication form led. Use of a Customer A TO BE PRINTED ON	2. For printing on the p (1) the names of up to or agents OR, alternative (2) the name of a single registered attorney or a 2 registered patent atto listed, no name will be TTHE PATENT (print or type data will appear on the p. OT a substitute for filing an (B) RESIDENCE: (CITY	3 registered patentely, e firm (having as a ggent) and the nameneys or agents. If a printed. be) atent. If an assignoassignment.	t attorneys 1 member a 2 es of up to no name is 3 ee is identified belo	w, the do	cument has been filed for
Please check the appropri	iate assignee category or	categories (will not be	printed on the patent):	Individual 🖵 Co	rporation or other p	rivate grou	ip entity 🗖 Government
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	s SMALL ENTITY statu	is. See 37 CFR 1.27.	☐ b. Applicant is no longed from anyone other than the				
interest as shown by the r	records of the United Sta	tes Patent and Tradema	rk Office.	не аррисані; а геді:	sicied autorney of ag	gent; or the	assignee or other party in
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This collection of informa an application. Confident submitting the completed this form and/or suggestion Box 1450, Alexandria, V.	ation is required by 37 C iality is governed by 35 I application form to the ons for reducing this bu- irginia 22313-1450. DC	FR 1.311. The informat U.S.C. 122 and 37 CFF USPTO. Time will var rden, should be sent to t ONOT SEND FEES OR	ion is required to obtain or r R 1.14. This collection is est y depending upon the indiv he Chief Information Office COMPLETED FORMS TO	etain a benefit by th imated to take 12 r idual case. Any co rr, U.S. Patent and ' D THIS ADDRESS	he public which is to ninutes to complete, mments on the amo Trademark Office, U SEND TO: Comm	o file (and , including ount of tim J.S. Depar hissioner fo	by the USPTO to process; gathering, preparing, and e you require to complete tment of Commerce, P.O. or Patents, P.O. Box 1450.

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		DUDE TRACTURE		

BIRCH STEWART KOLASCH & BIRCH PO BOX 747 FALLS CHURCH, VA 22040-0747

2871 DATE MAILED: 04/11/2008

ARTINIT

Determination of Patent Term Adjustment under 35 U.S.C. 154 (b)

(application filed on or after May 29, 2000)

The Patent Term Adjustment to date is 35 day(s). If the issue fee is paid on the date that is three months after the mailing date of this notice and the patent issues on the Tuesday before the date that is 28 weeks (six and a half months) after the mailing date of this notice, the Patent Term Adjustment will be 35 day(s).

If a Continued Prosecution Application (CPA) was filed in the above-identified application, the filing date that determines Patent Term Adjustment is the filing date of the most recent CPA.

Applicant will be able to obtain more detailed information by accessing the Patent Application Information Retrieval (PAIR) WEB site (http://pair.uspto.gov).

Any questions regarding the Patent Term Extension or Adjustment determination should be directed to the Office of Patent Legal Administration at (571)-272-7702. Questions relating to issue and publication fee payments should be directed to the Customer Service Center of the Office of Patent Publication at 1-(888)-786-0101 or (571)-272-4200.

Application No.	Applicant(s)				
09/677,870	CHOI ET AL.				
Examiner	Art Unit				
TIMOTHY RUDE	2871				
ars on the cover sheet with the co (OR REMAINS) CLOSED in this apport of other appropriate communication GHTS. This application is subject to and MPEP 1308.	plication. If not included				
<u> December 2007</u> .					
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5. ☐ Notice of Informal P 6. ☐ Interview Summary Paper No./Mail Dat 7. ☐ Examiner's Amendr 8. ☑ Examiner's Stateme 9. ☐ Other	(PTO-413), te				
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Art Unit: 2871

DETAILED ACTION

Claims

Claims 1, 4, and 10 are amended. Claims 12, 13, 15-18 and 24 are canceled by Applicant.

Specification

The specification is amended. Objection to the disclosure is withdrawn.

Allowable Subject Matter

Claims 1, 3-10, and 21-23 are allowed.

The following is an examiner's statement of reasons for allowance:

All claims are directly or indirectly dependent upon claim 1 with the following allowable subject matter. A thorough search of relevant prior art of record did not disclose, alone or in combination, a thin film transistor substrate with a circuit repair feature in a liquid crystal display, as claimed, comprising a plurality of gate dummy patterns, wherein a first gate dummy pattern is located beneath only one side of a data line and running parallel to the data line and also located below only one side of a pixel electrode that is located adjacent to the one side of the data line, a second gate dummy pattern is located beneath only the other side of the data line and running parallel to the

data line and also located below only one side of a pixel electrode that is located adjacent to the other side of the data line, the first and second gate dummy patterns being spaced from one another along their length.

The closest combination is Kim et al (Kim) USPAT 6,429,909 B1 in view of Song et al (Song) USPAT 6,313,889 B1.

Kim discloses in the third embodiment, Figures 6 and 7, (col. 6, line 35 through col. 7, line 5), a thin film transistor substrate in a liquid crystal display substrate comprising: pixel electrodes [col. 3, lines 17-21], data lines, 400, adjacent to each of two opposed sides of the pixel electrodes for applying data signals to the pixel electrodes via thin film transistors on the substrate [Abstract and Background of the Invention], gate lines, 100, for applying a gate signal, and an ITO [col. 5, lines 1-5] transparent pixel electrode, 600, for driving a liquid crystal cell, repair lines, 110 and 120, (Applicant's gate dummy patterns) adjacent to each of two opposed sides of the pixel electrodes, formed of the same material layer as the gate line (col. 6, lines 38-42 and col. 2, lines 10-17) and formed parallel to said data line, 100, and to overlap the pixel electrode, 600, and data line 400, to compensate for misalignment occurring along the data line.

Wherein the gate dummy pattern is formed to overlap [L3 and L4] with an edge portion of the pixel electrode (per Figures 6 and 7), and is separated by gate insulating

Application/Control Number: 09/677,870

Art Unit: 2871

layer, 200, thereby resulting in an aperture ratio increase of from about 5 to 6% per Applicant's enabling disclosure, page 8, lines 25-31.

FIG.6

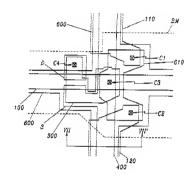
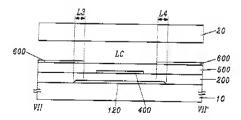


FIG.7



Application/Control Number: 09/677,870 Page 5

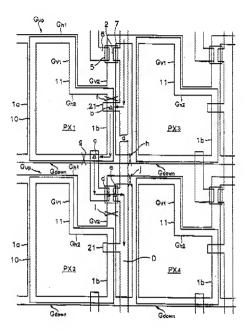
Art Unit: 2871

Song teaches the use of a redundant pattern that is integrated with the gate line and branches from the gate line in Figure 19A (G_{up} and G_{down} or 1a and 1b) wherein a dummy pattern extends substantially the entire length of the pixel electrode portion adjacent and parallel to the data line [portion running under right edge of pixel electrode in Figure 19A from below Gv2 to the horizontal portion between g and h] wherein the gate dummy patterns are physically located separate and apart from the gate lines [separated by cutting at the "X" marks, f and g, in Figure 19A], as a redundancy electrode for electrically connecting the gate line to the broken data line (col. 17, lines 4-67, especially col. 17, lines 47-54) to effect repairs.

Application/Control Number: 09/677,870

Art Unit: 2871

FIG. 19A



Kim teaches repair lines, 110 and 120, (Applicant's gate dummy pattern) formed of the same material layer as the gate line (col. 6, lines 38-42 and col. 2, lines 10-17) so as to extended vertically from the gate line, 100 (separated from data line, 400, by gate insulating layer, 200), and to overlap the pixel electrode, 600, and data line 400, to compensate for misalignment occurring along the data line.

However, no prior art was found with proper motivation to combine that would render the particular design specific structural limitations of gate dummy pattern shape,

Art Unit: 2871

locations, and overlap with other structures. Please note similarities in cited but not applied Sukenori et al USPAT 5,943,106 and Kaneko USPAT 6,587,162. These are considered relevant but are not considered sufficient to support a proper obviousness rejection.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to TIMOTHY RUDE whose telephone number is (571)272-2301. The examiner can normally be reached on Increased Flex Time Program.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Nelms C. David can be reached on (571) 272-1787. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Application/Control Number: 09/677,870 Page 8

Art Unit: 2871

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

tlr

/TIMOTHY RUDE/

Examiner, Art Unit 2871